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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Shinji TANAKA

Confirmation Number: 8196

Application No.: 10/682,153

Group Art Unit: 2824

Filed: October 10, 2003

Examiner: Hien N. Nguyen

For: SEMICONDUCTOR MEMORY DEVICE INPUTTING/OUTPUTTING DATA AND

PARITY DATA IN BURST OPERATION

STATUS INQUIRY

Mail Stop Publication Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

A Part B Fee Transmittal with Issue Fee and Publication Fee was filed in the referenced application on July 28, 2005. Since more than six months have elapsed since that date, and no response to said Part B Fee Transmittal has been received by attorney for applicant, Applicant respectfully requests that the undersigned attorney be informed of the status of the referenced application.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

Stephen A. Becker Registration No. 26,527

600 13th Street, N.W. Washington, DC 20005-3096 Phone: 202.756.8000 SAB:JGH

Facsimile: 202.756.8087 Date: March 1, 2006

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